



Prevas Sierra RTOS

The Fastest Real-Time Kernel in world!

Xilinx Alliance Embedded Partner

>> Prevas Sierra is a **unique** implementation of a real-time kernel in hardware and thanks to this, much faster than any software RTOS on the market.



XILINX EDITION

The Prevas Sierra stand-alone RTOS supports both the IBM PowerPC and Xilinx MicroBlaze processors in Xilinx Virtex and Spartan Family FPGAs.

Device Family Support

- ✦ Virtex-4
- ✦ Virtex-II Pro
- ✦ Virtex-II
- ✦ Virtex-E
- ✦ Spartan-3E
- ✦ Spartan-3
- ✦ Spartan-IIE
- ✦ Spartan-II

Evaluation

Sierra Configuration

- ✦ 16 tasks
- ✦ 8 priority levels
- ✦ 16 binary semaphores
- ✦ 4 flags
- ✦ 8 external interrupts
- ✦ Timers for delay, periodic tasks and watchdogs

More Information

- ✦ [Contact us](#)

Xilinx Software Support

- ✦ Xilinx EDK v 7.1i and higher
- ✦ Xilinx EDK v 6.3i
- ✦ Xilinx EDK v 6.2i
- ✦ Xilinx EDK v 6.1i

>> Multiprocessor support

Sierra statistics and info

- ✦ [Benchmark numbers and suitable target systems](#)
- ✦ Gate count, performance; see [datasheet](#)

Sierra Key Features

- ✦ Interfaces to Core Connect OPB and LMB busses
- ✦ Works in parallel to the CPU
- ✦ Scalable micro-kernel structure
- ✦ Deterministic - 100 % time predictable
- ✦ Plug-in architecture for adding of new system components
- ✦ Full board support package with UDP, file system etc.

Delivery

- ✦ Sierra as a netlist or vhd
- ✦ Source code API, C-code interface (only 2 Kb)
- ✦ User Reference Manual
- ✦ Hardware Manual
- ✦ Program example

